

# Agenda

- 1) Review
- 2) Computer representation of sets
- 3) Set and Logic Algebra
- 4) Digital circuits

## Review

Sets: unique unordered

set builder notation, venn diagrams

set operation  $\cup, \cap, \subset, -, \Delta$

cardinality & powersets

## Exercise:

1)  $A = \{ \{1, 2\}, 3, 4 \}$        $B = \{ 1, 2, \{3\} \}$

a) what is  $A \cup B$ ?

$$\{ \{1, 2\}, 3, 4, 1, 2 \}$$

b) what is  $|A \cap B|$ ?  $A \cap B = \{ \{3\} \}$

Bonus c)  $\{1, 2\} \in A - B$ ?  $\{ \{1, 2\}, 4 \}$

Yes

Bonus d)  $\{1, 2\} \subseteq A \Delta B$ ?  $\{ \{1, 2\}, 4, \{1, 2\} \}$

Yes

# Representing sets on computers

Remember how we figured out power sets?

$$A = \{a, b\}$$

$$\mathcal{P}(A) = \{\{a\}, \{b\}, \{a, b\}, \emptyset\}$$

"  $\{b, a\}$

a	b	$\mathcal{P}(\{a, b\})$
F	F	$\emptyset$
F	T	$\{b\}$
T	F	$\{a\}$
T	T	$\{a, b\}$

Computers like things that are binary and fit in a set amount of bits  
How can we do this for sets?

1	2		
a	b	$\mathcal{P}(A)$	
F $\Rightarrow$ 0	F $\Rightarrow$ 0	$\emptyset$	$\rightarrow$ 00
F $\Rightarrow$ 0	T $\Rightarrow$ 1	$\{b\}$	$\rightarrow$ 01
T $\Rightarrow$ 1	F $\Rightarrow$ 0	$\{a\}$	$\rightarrow$ 10
T $\Rightarrow$ 1	T $\Rightarrow$ 1	$\{a, b\}$	$\rightarrow$ 11

step 1  $\rightarrow$  assign index position to all elements in universe

step 2  $\rightarrow$  if item 1 in set  $\Rightarrow$  index i set to 1  
not in set  $\Rightarrow$  index i set to 0

e.g.  $U = \{ \underset{1}{\text{green}}, \underset{2}{\text{black}}, \underset{3}{\text{red}}, \underset{4}{\text{white}}, \underset{5}{\text{blue}} \}$

$A = \{ \underset{0}{\text{black}}, \underset{1}{\text{red}}, \underset{1}{\text{blue}} \}$

Computer store # of bits = |Universe|  $A = 01101$   $B = 10011$

Connecting set operators to logic op

$U = \{ \underset{1}{\text{green}}, \underset{2}{\text{black}}, \underset{3}{\text{red}}, \underset{4}{\text{white}}, \underset{5}{\text{blue}} \}$

$A = \{ \text{black, red, blue} \}$   
 $a = 01101$

$B = \{ \text{green, black, blue} \}$   
 $b = 11001$

$A \cup B = \{ \text{green, black, red, blue} \}$   
 $11101$

$\begin{array}{r} 01101 \\ \text{OR } 11001 \\ \hline 11101 \end{array}$

Sets

$A^c = \{ \text{green, white} \}$

$\bar{A}$



Logic

$a = 01101$   
 $\neg a = 10010$  each bit negated

$A \cup B$

$a = 01101$   
 $b = 11001$   
 $a \cup b = 11101$  apply OR to each bit

$$A \cap B = \{ \text{black, blue} \}$$

$$\begin{aligned} a &= 01101 \\ b &= 11001 \\ a \cap b &= 01001 \end{aligned}$$

apply and  
to each bit

$$A \Delta B = \{ \text{green, red} \}$$

$$\begin{aligned} a &= 01101 \\ b &= 11001 \\ a \oplus b &= 10100 \end{aligned}$$

apply xor  
to each bit

### Connecting set/logic operators

<u>Set</u>	<u>Logic</u>
$\bar{\phantom{A}}$ (complement)	$\neg$ (negation)
$\cap$ (intersection)	$\wedge$ (and)
$\cup$ (union)	$\vee$ (or)
$\Delta$ (sym. diff.)	XOR

# Set Algebra (Logic Algebra)

algebra ....

$$x(x+10) - 2x + 15$$

$$x^2 + 10x - 2x + 15$$

$$x^2 + 8x + 15$$

(distributive law)

manipulating expressions to simplify them.

Can do same for Logic / Set expressions!

We have a few rules to help us.

$$(q + 1) + s = q + (1 + s)$$

Algebra

$$(x + y) + z = x + (y + z)$$

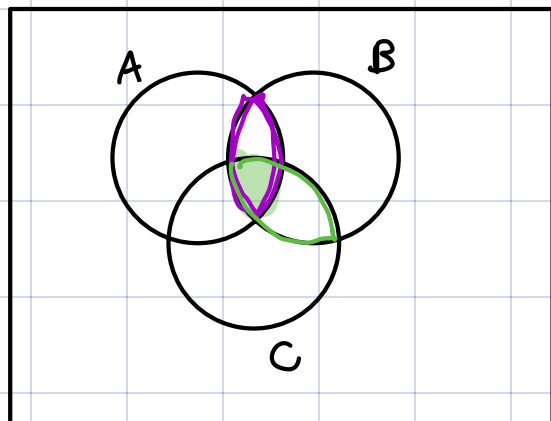
P	q	r	$(p \vee q) \vee r$	$p \vee (q \vee r)$
F	F	F	F	F
F	F	T	T	T
F	T	F	T	T
F	T	T	T	T

Logic

**Associative Laws**

$$(P \vee Q) \vee R = P \vee (Q \vee R)$$

$$(P \wedge Q) \wedge R = P \wedge (Q \wedge R)$$



Sets

$$(A \cup B) \cup C = A \cup (B \cup C)$$

$$(A \cap B) \cap C = A \cap (B \cap C)$$

# Other Rules

## Double Negation

$$\neg\neg P = P$$

$$T \rightarrow F \rightarrow T$$

$$(A^C)^C = A$$

## Distributive Laws

$$P \wedge (Q \vee R) = (P \wedge Q) \vee (P \wedge R)$$

$$P \vee (Q \wedge R) = (P \vee Q) \wedge (P \vee R)$$

$$A \cap (B \cup C) = (A \cap B) \cup (A \cap C)$$

$$A \cup (B \cap C) = (A \cup B) \cap (A \cup C)$$

Idea:  $x * (y + z) = x * y + x * z$

## Absorption Laws

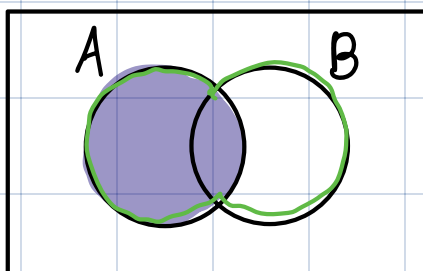
$$P \wedge (P \vee Q) = P$$

$$P \vee (P \wedge Q) = P$$

$$A \cap (A \cup B) = A$$

$$A \cup (A \cap B) = A$$

Idea:



:

## Complement Laws

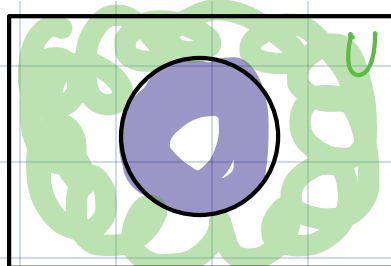
$$P \vee \neg P = T$$

$$P \wedge \neg P = F$$

$$A \cup A^C = U \quad \leftarrow \text{universe}$$

$$A \cap A^C = \emptyset \quad \leftarrow \emptyset = \{\}$$

Idea:



# Idempotent Laws (you have overcomplicated things)

$P \vee P = P$   
 $P \wedge P = P$

$A \cup A = A$   
 $A \cap A = A$

## Identity

idea:

False  $\vee P = P$   
True  $\wedge P = P$

P	$P \vee F$	$P \wedge T$	$P \wedge F$	$P \vee T$
F	F	F	F	T
T	T	T	F	T

$\emptyset \cup A = A$   
 $U \cap A = A$

## Domination:

True  $\vee P = \text{True}$   
 False  $\wedge P = \text{False}$

$U \cup A = U$   
 $\emptyset \cap A = \emptyset$

p does not matter

## DeMorgan's Laws

$\neg(P \vee Q) = \neg P \wedge \neg Q$   
 $\neg(P \wedge Q) = \neg P \vee \neg Q$

$(A \cup B)^c = A^c \cap B^c$   
 $(A \cap B)^c = A^c \cup B^c$

## Recall from Day 4

Exercise Build Truth tables for  
 1)  $\neg(A \vee B)$       2)  $\neg A \wedge \neg B$

A	B	$A \vee B$	$\neg(A \vee B)$	A	B	$\neg A$	$\neg B$	$\neg A \wedge \neg B$
F	F	F	T	F	F	T	T	T
F	T	T	F	F	T	T	F	F
T	F	T	F	T	F	F	T	F
T	T	T	F	T	T	F	F	F

two statements are logically equivalent

# Simplifying Boolean or set expressions

Ex]  $(x \cup y) \cap (x \bar{y})$   $xy + xz$   
 $x(y+z)$

(distributive law)

$x \cup (y \cap \bar{y})$

(complement law)

$x \cup \emptyset$

(identity law)

X

Ex]  $\neg(\neg A \vee B) \wedge \neg B$

$(\neg\neg A \wedge \neg B) \wedge \neg B$  De Morgan

$(A \wedge \neg B) \wedge \neg B$  (double negation)

$A \wedge (\neg B \wedge \neg B)$  (associative)

$A \wedge \neg B$  (idempotent)

Practice 1)  $(A \cup B) \cap \bar{A}$

$(A \cap \bar{A}) \cup (B \cap \bar{A})$  dist.

$\emptyset \cup (B \cap \bar{A})$  compl.

$B \cap \bar{A}$  identity



$$2) \quad (\overline{1x1x}) \vee (y \vee \overline{1x})$$

$$F \vee (y \vee \overline{1x})$$

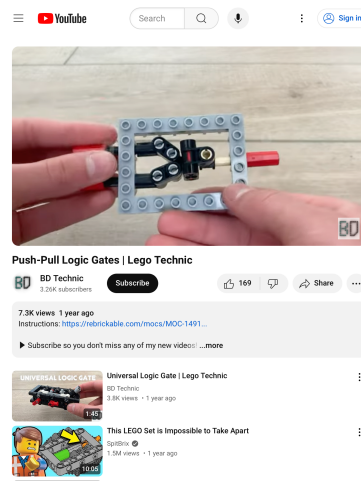
$$F \vee (y \vee x)$$

$$y \vee x$$

complement  
double neg  
identity

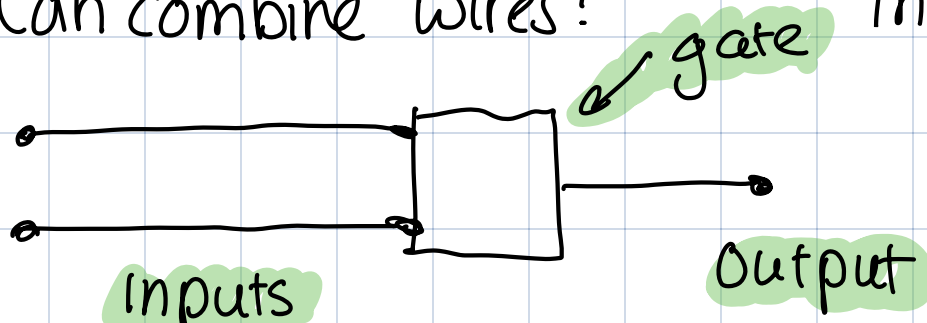
## Circuits on Computers

[https://youtu.be/RA2po1xk\\_0A?t=5](https://youtu.be/RA2po1xk_0A?t=5)



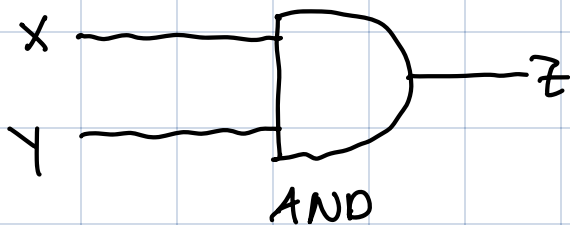
wire  
electricity = 1  
no electricity = 0

Can combine wires!



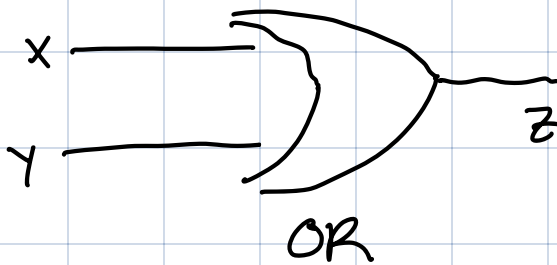
This forms a.

Each of our boolean operators has a corresponding gate

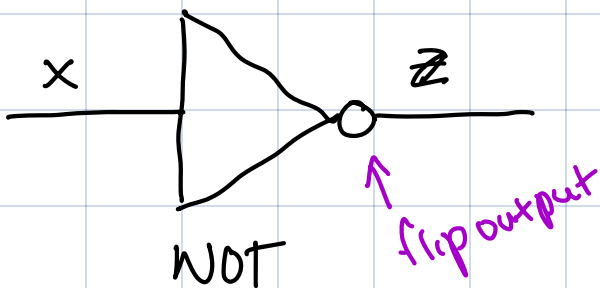


X	Y	Z
0	0	0
0	1	0
1	0	0
1	1	1

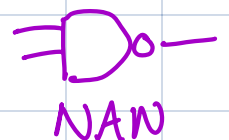
Note when talking about circuits we use 0/1 instead of T/F

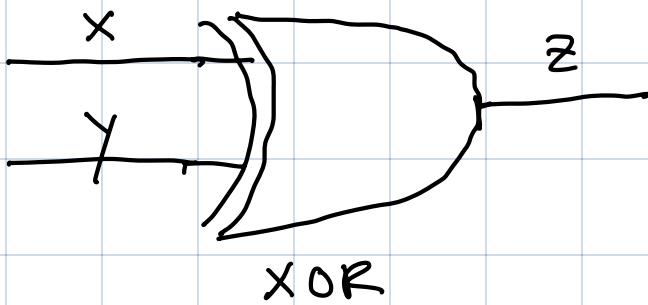


X	Y	Z
0	0	0
0	1	1
1	0	1
1	1	1



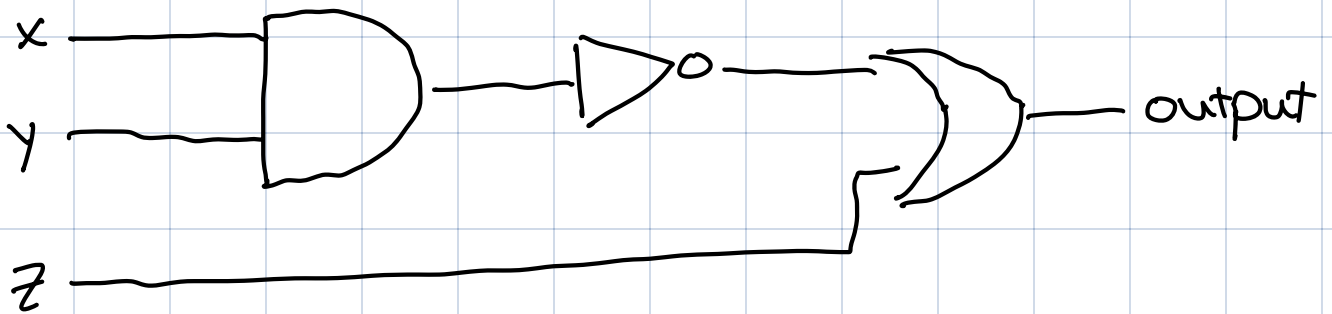
X	Z
0	1
1	0





x	y	z
0	0	0
0	1	1
1	0	1
1	1	0

Circuits are when we connect gates....

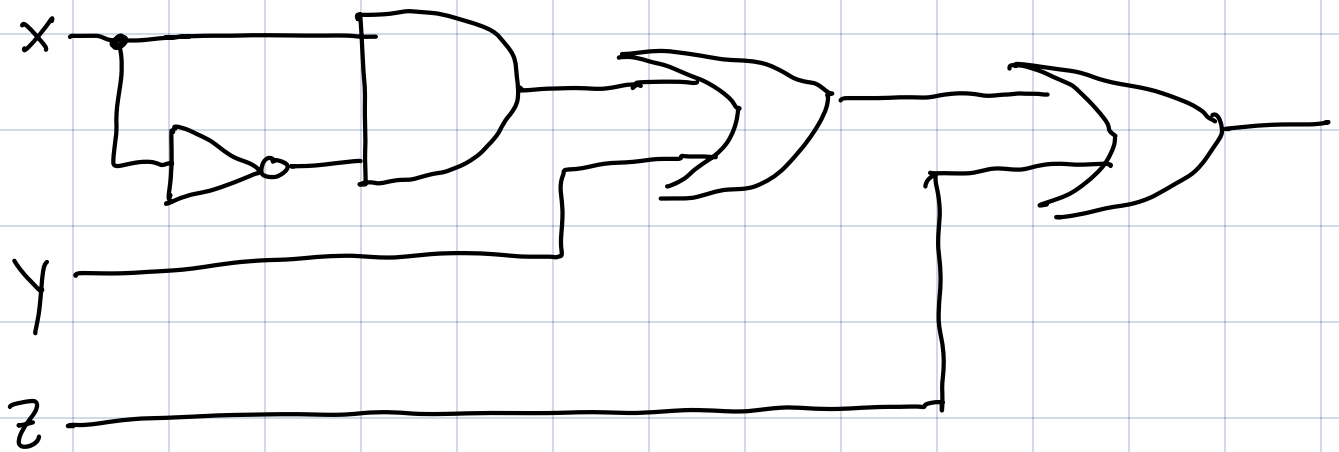


The logic expression above is...

$$\neg(x \wedge y) \vee z$$

Hint: work left to right when applying operations and remember your ()

## Exercise:



1) express using logic

$$(x \wedge \neg x) \vee y \vee z$$

2) simplify above expression using logic rules

$$((x \wedge \neg x) \vee y) \vee z$$

$$F \vee (y \vee z) \quad \text{comp}$$

$$y \vee z \quad \text{ident}$$

3) Draw simplified expression

